



COPY OF PAPERS  
ORIGINALLY FILED

Attorney's Docket No.: 10417-100001 / F51-136641M/TOM

2818

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Haruo Hyodo et al.                      Art Unit : 2818  
Serial No. : 09/963,839                              Examiner : Dung Anh Le  
Filed : September 26, 2001  
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEROF

5/ Elect  
G. Stealy  
827-02

Commissioner for Patents  
Washington, D.C. 20231

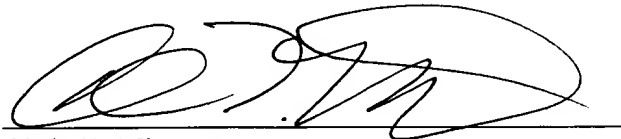
RESPONSE TO RESTRICTION REQUIREMENT

Responsive to the action mailed June 25, 2002, applicant elects the invention of Group I (claims 1 to 7) drawn to a semiconductor device. The election is made without traverse.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 7/22/02

  
Chris T. Mizumoto  
Reg. No. 42,899

Fish & Richardson P.C.  
4350 La Jolla Village Drive, Suite 500  
San Diego, California 92122  
Telephone: (858) 678-5070  
Facsimile: (858) 678-5099

10199999.doc

RECEIVED  
AUG 21 2002  
TECHNOLOGY CENTER 2800

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

July 22, 2002  
Date of Deposit  
Brian Speer  
Signature  
Brian Speer  
Typed or Printed Name of Person Signing Certificate